Release notes SYGDG1-SYGDG1B

These release notes are applicable to the following modules:

- GDS010
- GDS100
- GDS110
- GSU010
- GSU100
- GSU110
- GXG010
- GXG100
- GXG110
- HDS010
- HDS100
- HDS110
- HSU010
- HSU100
- HSU110
- HXH010
- HXH100
- HXH110

Software version	7713
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	10-5-2019
Filename	SYGDG1_1-0301to0304-7713-SINGLE-service.zip

- Added Follow option to the Inserter preset.

Software version	7713
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	10-5-2019
Filename	SYGDG1_1-0100to0201-7713-SINGLE-service.zip

- Added Follow option to the Inserter preset.

Software version	7613
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	18-12-2018
Filename	SYGDG1_1-0301to0304-7613-SINGLE-service.zip

Fixed:
- Disturbed embedded audio on both SDI output path A and B

Software version	7613
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	18-12-2018
Filename	SYGDG1_1-0100to0201-7613-SINGLE-service.zip

Fixed:
- Disturbed embedded audio on both SDI output path A and B

Software version	7611
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	26-06-2018
Filename	SYGDG1_1-0301to0304-7611-SINGLE-service.zip

Fixes:
- When converting from P to I Dolby E can be placed in wrong field
- Lock tri-level 1080i50 to 1080p50 video signal
- Video to ref timing incorrect, when switching output mapping between 1080p50 level A and level B

Software version	7611
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	26-06-2018
Filename	SYGDG1_1-0100to0201-7611-SINGLE-service.zip

Fixes:
- When converting from P to I Dolby E can be placed in wrong field
- Lock tri-level 1080i50 to 1080p50 video signal
- Video to ref timing incorrect, when switching output mapping between 1080p50 level A and level B

Software version	7509
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	08-03-2018
Filename	SYGDG1_1-0301to0304-7509-SINGLE-service.zip

Timecode debugged. It can now handle downcounting ATC of type LTC including framecounter
Timecode VITC changed. It now inserts VITC#1 in field 1 and VITC#2 in field 2.

Software version	7509
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	08-03-2018
Filename	SYGDG1_1-0100to0201-7509-SINGLE-service.zip

Timecode debugged. It can now handle downcounting ATC of type LTC including framecounter
Timecode VITC changed. It now inserts VITC#1 in field 1 and VITC#2 in field 2.

Software version	7507
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	02-03-2018
Filename	SYGDG1_1-0301to0304-7507-SINGLE-service.zip

Timecode improved, it can now handle downcounting ATC of type LTC

Software version	7507
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	02-03-2018
Filename	SYGDG1_1-0100to0201-7507-SINGLE-service.zip

Timecode improved, it can now handle downcounting ATC of type LTC

Software version	7505
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	20-11-2017
Filename	SYGDG1_1-0301to0304-7505-SINGLE-service.zip

OP47-WST input conversion could get out of sequence caused by Power-up or field drop/repeat or data misinterpretation.
Solved by changing fifo memory to fixed addressable memory.

Software version	7405
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	20-11-2017
Filename	SYGDG1_1-0100to0201-7505-SINGLE-service.zip

OP47-WST input conversion could get out of sequence caused by Power-up or field drop/repeat or data misinterpretation.
Solved by changing fifo memory to fixed addressable memory.

Software version	7499
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	13-10-2016
Filename	SYGDG1_1-0100to0201-7499-SINGLE-service.zip

Fixed lip sync issue for 1080p50 to 1080i50 cross conversion (also applies to 1080p60 to 1080i60 cross conversion).
Fixed power cycle issue with level-B output format. Now the card will start up in level-B instead of level-A, when previously set to level-B output format.
Fixed level-B video to reference timing
Fixed picture quality issue for 3G to SD-SDI down conversion
Added manual (ACP) override for automatically controlled presets (i.e. s2016), like recently added in GXT.
Fixed missing or corrupt pixel, appearing as a randomly coloured line on the right hand side of the picture when downconverting 10080i to SD.
Fixed malfunctioning zooming and or positioning.
Fixed OP47 line insertion number.
Fixed sporadic audio loss when switching on the card's input.
Fixed malfunctioning up conversion for 60Hz signals.

Software version	6893
Hardware version	0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	10-03-2015
Filename	SYGDG1_1-0301to0304-6893-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Frame or field accurate aspect ratio switching on S2016, WSS or VI data (once fixed in recalled release 6582/6583).
Fixed: Frame or field accurate S2016, WSS and VI data insertion (once fixed in recalled release 6582/6583).
Fixed: Inserter preset master was coupled to the video inserter preset master, resulting in malfunctioning inserter (introduced in recalled release 6582/6583).
Fixed: GPI translation did no longer function (introduced in recalled release 6582/6583).
Fixed: Broken audio when connecting 2 sources, occurs in release 6582/6583 only.
Fixed: GPI latched / nonlatched was working reversed.
Added: GPI input debouncing time setting.
Fixed: Occasional occurence of a black, corrupted or old video frame.
Fixed: OP47 and WST transcoding and buffering. Now, the card is able to handle 5 SDP packets per frame, and correctly tranlsate it into WST or OP47 when applicable. This should fix sporadically missing subtitle updates.
Fixed: Robustness for OP47 and WST handling when packets are inserted into the wrong field.

Software version	6893
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	10-03-2015
Filename	SYGDG1_1-0100to0201-6893-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Frame or field accurate aspect ratio switching on S2016, WSS or VI data (once fixed in recalled release 6582/6583).
Fixed: Frame or field accurate S2016, WSS and VI data insertion (once fixed in recalled release 6582/6583).
Fixed: Inserter preset master was coupled to the video inserter preset master, resulting in malfunctioning inserter (introduced in recalled release 6582/6583).
Fixed: GPI translation did no longer function (introduced in recalled release 6582/6583).
Fixed: Broken audio when connecting 2 sources, occurs in release 6582/6583 only.
Fixed: GPI latched / nonlatched was working reversed.
Added: GPI input debouncing time setting.
Fixed: Occasional occurence of a black, corrupted or old video frame.
Fixed: OP47 and WST transcoding and buffering. Now, the card is able to handle 5 SDP packets per frame, and correctly tranlsate it into WST or OP47 when applicable. This should fix sporadically missing subtitle updates.
Fixed: Robustness for OP47 and WST handling when packets are inserted into the wrong field.

Software version	6583
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	11-2-2014
Filename	SYGDG1_1-0100to0201-6583-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Frame or field accurate aspect ratio switching on S2016 AFD data.
Fixed: Frame or field accurate S2016 AFD data insertion.

Software version	6583
Hardware version	0300, 0301, 0302, 0303, 0304
Platform	SYGDG1B
Date	11-2-2014
Filename	SYGDG1_1-0300to0304-6583-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Frame or field accurate aspect ratio switching on S2016 AFD data.
Fixed: Frame or field accurate S2016 AFD data insertion.

Software version	6379
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	6-12-2013
Filename	SYGDG1_1-0100to0201-6379-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Improved deinterlacing performance for SD625 and SD525 to 1080p and 720p format conversions.
Fixed: Improved deinterlacing performance for 1080i to 1080p and 720p format conversions.
Fixed: p24 conversions.
Fixed: Cortex Layout Forms updated / aligned for single and twins versions.
Added: 1035i60 conversions.
Added: Menu items in Cortex changed.
Added: Ouput format control added.

Software version	6379
Hardware version	0300, 0301, 0302, 0303
Platform	SYGDG1B
Date	6-12-2013
Filename	SYGDG1_1-0300to0303-6379-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Improved deinterlacing performance for SD625 and SD525 to 1080p and 720p format conversions.
Fixed: Improved deinterlacing performance for 1080i to 1080p and 720p format conversions.
Fixed: p24 conversions.
Fixed: Cortex Layout Forms updated / aligned for single and twins versions.
Added: 1035i60 conversions.
Added: Menu items in Cortex changed.
Added: Ouput format control added.

Software version	6377
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	2-12-2013
Filename	SYGDG1_1-0100to0201-6377-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: p24 conversions.
Fixed: Audio nonPCM.
Fixed: Cortex Layout Forms updated / aligned for single and twins versions.
Added: 1035i60 conversions.
Added: Menu items in cortex changed.
Added: Ouput control Auto/Manual Output.

Software version	6377
Hardware version	0300, 0301, 0302, 0303
Platform	SYGDG1B
Date	2-12-2013
Filename	SYGDG1_1-0300to0303-6377-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: p24 conversions.
Fixed: Audio nonPCM.
Fixed: Cortex Layout Forms updated / aligned for single and twins versions.
Added: 1035i60 conversions.
Added: Menu items in cortex changed.
Added: Ouput control Auto/Manual Output

Software version	6277
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	27-11-2013
Filename	SYGDG1_1-0100to0201-6277-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: p24 conversions.
Fixed: Audio nonPCM.
Added: 1035i60 conversions.
Added: Menu items in cortex changed.
Added: Ouput control Auto/Manual Output

Software version	6277
Hardware version	0300, 0301, 0302, 0303
Platform	SYGDG1B
Date	27-11-2013
Filename	SYGDG1_1-0300to0303-6277-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: p24 conversions.
Fixed: Audio nonPCM.
Added: 1035i60 conversions.
Added: Menu items in cortex changed.
Added: Ouput control Auto/Manual Output

Software version	6275
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	15-11-2013
Filename	SYGDG1_1-0100to0201-6275-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: p24 conversions.
Added: 1035i60 conversions.
Added: Menu items in cortex changed.
Added: Ouput control Auto/Manual Output

Software version	6275
Hardware version	0300, 0301, 0302, 0303
Platform	SYGDG1B
Date	15-11-2013
Filename	SYGDG1_1-0300to0303-6275-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: p24 conversions.
Added: 1035i60 conversions.
Added: Menu items in cortex changed.
Added: Ouput control Auto/Manual Output

Software version	5967
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	19-06-2013
Filename	SYGDG1_1-0100to0201-5967-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Clean switching behavior between different ARC settings.
Fixed: p25 and p30 output formats. Half of the image was missing.

Software version	5967
Hardware version	0301, 0302
Platform	SYGDG1B
Date	19-06-2013
Filename	SYGDG1_1-0301to0302-5967-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Clean switching behavior between different ARC settings.
Fixed: p25 and p30 output formats. Half of the image was missing.

Software version	5863
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	16-04-2013
Filename	SYGDG1_1-0100to0201-5863-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Dolby Guard band allignment. Now, when the dolby guard band is at the right place in time, the guard band is maintained, even if the card is up-,down- or cross-converting video formats.
Fixed: Audio fade in/out due to a framedrop or repeat is now disabled for non-PCM signals, preventing data corruption.
Fixed: The audio synchroniser tracks the video delay more accurately now, resyncing the audio with the video if a 2 sample discrepancy is reached. This used to be 32 samples. Now, some hysteresis is maintained, whilst keeping the guard band in place for freerunning inputs with Dolby E.
Fixed: Occasionally corrupt audio after switching signals on the input.
Fixed: The Aspect Ratio flag bit for s2016 output is now set correctly according the video output format.
Fixed: Improved the granularity of the audio delay control setting. In practice the granularity is now 2 audio samples.
Fixed: Timecode insertion and conversion now accounts for the tracked processing delay of the video. This was malfunctioning since release 4545.
Fixed: Card startupbehaviour concerning the timecode when both VITC- and LTC-ATC packets are present in the signal.
Fixed: Timecode field flag is correctly de-embedded and re-inserted when converting timecode.

Software version	5863
Hardware version	0301, 0302
Platform	SYGDG1B
Date	16-04-2013
Filename	SYGDG1_1-0301to0302-5863-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: Dolby Guard band allignment. Now, when the dolby guard band is at the right place in time, the guard band is maintained, even if the card is up-,down- or cross-converting video formats.
Fixed: Audio fade in/out due to a framedrop or repeat is now disabled for non-PCM signals, preventing data corruption.
Fixed: The audio synchroniser tracks the video delay more accurately now, resyncing the audio with the video if a 2 sample discrepancy is reached. This used to be 32 samples. Now, some hysteresis is maintained, whilst keeping the guard band in place for freerunning inputs with Dolby E.
Fixed: Occasionally corrupt audio after switching signals on the input.
Fixed: The Aspect Ratio flag bit for s2016 output is now set correctly according the video output format.
Fixed: Improved the granularity of the audio delay control setting. In practice the granularity is now 2 audio samples.
Fixed: Timecode insertion and conversion now accounts for the tracked processing delay of the video. This was malfunctioning since release 4545.
Fixed: Card startupbehaviour concerning the timecode when both VITC- and LTC-ATC packets are present in the signal.
Fixed: Timecode field flag is correctly de-embedded and re-inserted when converting timecode.

Software version	5455
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	09-01-2013
Filename	SYGDG1_1-0100to0201-5455-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Fixed: VI input detection, which was malfunctioning since release 3732.

Software version	5353
Hardware version	0100, 0200, 0201
Platform	SYGDG1
Date	29-11-2012
Filename	SYGDG1_1-0100to0201-5353-SINGLE-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
Added: Horizontal and Vertical Positioning of the active video. Only applicable when zooming in, for instance when downconverting using Center-Cut Aspect Ratio Conversion.
Added: Seperate Audio Delay control for each de-embedder. Previously, only one delay control was available for all inputs.
Fixed: Occasional loss of audio after switching signals on the input.
Fixed: Occasional loss of video after switching signals on the input.
Fixed: Reference selection not working.
Fixed: website for re-programming Linux based cards.

Software version	4545
Hardware version	0100, 0200
Platform	SYGDG1
Date	10-08-2012
Filename	SYGDG1_1-0100to0200-4545-SINGLE-service.zip

Software version	4437
Hardware version	0100, 0200
Platform	SYGDG1
Date	26-06-2012
Filename	SYGDG1_1-0100to0200-4437-service.zip

Software version	3934
Hardware version	0100, 0200
Platform	SYGDG1
Date	06-02-2012
Filename	SYGDG1_1-0100to0200-3934-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Please find the Restricted Delivery Announcement included in this download.
Also included are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
- Added Level-B 3G output mapping on all inputs and outputs
- Solved Video flashes when there is no input on input2

Software version   3732

Hardware version   0100, 0200

Platform   SYGDG1

Date   07-12-2011

Filename   SYGDG1_1-0100to0200-3732-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Please find the Restricted Delivery Announcement included in this download.
Also included are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
- Added Level-B 3G output mapping

Software version   3030

Hardware version   0100, 0200

Platform   SYGDG1

Date   25-08-2011

Filename   SYGDG1_1-0100to0200-3030-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Please find the Restricted Delivery Announcement included in this download.
Also included are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
- Added: EDH error detection for SD inputs, CRC error detection for HD/3G inputs.
- Added: Audio format status.

Software version   2929

Hardware version   0100, 0200

Platform   SYGDG1

Date   09-08-2011

Filename   SYGDG1_1-0100to0200-2929-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Please find the Restricted Delivery Announcement included in this download.
Also included are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Bugs fixed/features added:
- Improved stability of the audio synchroniser.
- Added: Negative audio delay, so audio can be advanced if the video is delayed.
- Fixed: Occasional dissappearance of frame or card on the network.

Software version	2728
Hardware version	0100, 0200
Platform	SYGDG1
Date	08-07-2011
Filename	SYGDG1_1-0100to0200-2728-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Bugs fixed/features added:
- Handling of Closed Captioning.
- SD-SDI outputs lock to 1080i tri-level sync.
- OP47 detection improved for bursting data inputs.
- Extra Horizontal Low Pass Filter set added. (more sharpness, less ringing with some aliasing)
- Input lock to 720p standards.
- Audio-bus-IO order can be changed from 1234 to 1324 for twin cards.

Software version	2527
Hardware version	0100, 0200
Platform	SYGDG1
Date	16-06-2011
Filename	SYGDG1_1-0100to0200-2527-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Bugs fixed/features added:
- Downconverter vertical position corrected.
- Downconverter (scale dependant) last missing Cr sample corrected.
- Added "Anam-702" in Arc setting in Down- and Up-conversion.

Software version	2425
Hardware version	0100, 0200
Platform	SYGDG1
Date	06-05-2011
Filename	SYGDG1_1-0100to0200-2425-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Bugs fixed/features added:
- Solved VI insertion problem
- Added GPI C pool for BCD
- No take select needed for BCD GPI■s anymore

Software version	2222
Hardware version	0100, 0200
Platform	SYGDG1
Date	06-04-2011
Filename	SYGDG1_1-0100to0200-2222-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Bugs fixed:
Vertical scale of interlaced inputs. Was introduced by level-B handling.

Software version	1921
Hardware version	0100, 0200
Platform	SYGDG1
Date	31-01-2011
Filename	SYGDG1_1-0100to0200-1921-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Added features:
Level B (3GB/s active video only) handling on input and output.
Low Pass Filtering on Up, Down, Cross, and Transparent conversion.
Possible to select 6 VANC lines to be transparent.

Bugs fixed:
V position not correct.
GPI Pool offset.
Improved 720p down conversion quality

Software version	1619
Hardware version	0100, 0200
Platform	SYGDG1
Date	10-11-2010
Filename	SYGDG1_1-0100to0200-1619-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Also included are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Fixed bugs:
Clean aspect ratio switching added.
SD-SDI 625 in- and output line shift corected.
SD525 V-flag corrected in TRS.
1080p60 and 1080p50 V-flag corrected in TRS.
Line on the right side of the picture.

Added features:
Action on input loss.
Timecode per output channel.
Pre-filter (Low pass) filter added for downconversion.
OP47 to WST in downconversion and WST to OP47 in upconversion added.

Software version	1418
Hardware version	0100, 0200
Platform	SYGDG1
Date	30-07-2010
Filename	SYGDG1_1-0100to0200-1418-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

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Fixed bugs:
The VITC timecode frame counter was lagging the seconds counter.
The audio delay of the second channel differed from the audio delay of the first channel.
When switching the input signal, or connecting signal via a patch pannel, or booting up equipment feeding the card, the audio and/or video might stop working. Also, the channel order of the audio might get corrupted.
Colorbar generation in standards besides 1080i/1080p.
Min delay in delay mode corrected to 3 lines.
Reduced the number of audio CRC errors.
Audio channel order corrected in SD-SDI

Added features:
Added Sum to #Out-Mode setting.
Added freeze control.

Software version	0910
Hardware version	0100, 0200
Platform	SYGDG1
Date	14-12-2009
Filename	SYGDG1_1-0100to0200-0910-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup, for instructions please refer to the Synapse module reprogramming guide under Chapter 2 re-programming Linux based cards or contact support.

Please find the Restricted Delivery Announcement included in this download.
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Software version	0407
Hardware version	0100
Platform	SYGDG1
Date	07-09-2009
Filename	SYGDG1_1-0100-0407-service.zip

Software version	0404
Hardware version	0100
Platform	SYGDG1
Date	27-07-2009
Filename	SYGDG1_1-0100-0404-service.zip

WARNING: Updating from version 0303 or earlier might result in loss of product key.
Please contact support before updating the card, support will provide a new product key.

Please find the Restricted Delivery Announcement included in this download.

Fixed: Output to reference timing offset
Added: Time code transparency and conversion.
Added: Cortex support, Cortex CLF forms are included in this download. Copy to ..\Cortex\forms\device*.clf for usage.
Fixed: Occasional faulty color balance in SD transparent mode.
Added: 16 channel audio transparency in A-only and B-only mode.
Added: S2016 support for HD in- and output.