

Release notes SYGDG1-SYGDG1B

These release notes are applicable to the following modules:

- 2GF100 2GF110 2HF100 2HF110 2SF100 2SF110

- GFS010 GFS100 GFS110 HFS010
- HFS100 HFS110

- SFS010 SFS100
- SFS110

Software version 6246

Hardware version 0301, 0302, 0303, 0304

SYGDG1B Platform 4-6-2019 Date

Filename SYGDG1_2-0301to0304-6246-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Fixed wrong generated menu in previous version
Solved: Output B would break when having format on auto or switching formats.
RDA: Delay for all twin products are half of what is mentioned in the manual.
1080 signals is 15 frames maximum
720 signals is 30 frames maximum
SD signals is 62 frames maximum

Software version 6246

Hardware version 0100, 0200, 0201

Platform SYGDG1 Date 4-6-2019

Filename SYGDG1_2-0100to0201-6246-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Fixed wrong generated menu in previous version



0301, 0302, 0303, 0304 Hardware version

Platform SYGDG1B 25-3-2019 Date

Filename SYGDG1_2-0301to0304-6146-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Solved: Output B would break when having format on auto or switching formats. RDA: Delay for all twin products are half of what is mentioned in the manual. 1080 signals is 15 frames maximum 720 signals is 30 frames maximum SD signals is 62 frames maximum

Software version 6146

0100, 0200, 0201 Hardware version

Platform SYGDG1 Date 25-3-2019

Filename SYGDG1_2-0100to0201-6146-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Solved: Output B would break when having format on auto or switching formats. RDA: Delay for all twin products are half of what is mentioned in the manual. 1080 signals is 15 frames maximum 720 signals is 30 frames maximum SD signals is 62 frames maximum

Software version 6045

0301, 0302, 0303, 0304 Hardware version

Platform SYGDG1B Date 07-05-2018

SYGDG1_2-0301to0304-6045-service.zip **Filename**

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Solved: When the second input is disturbed or disconnected the the first output was disturbed

Software version 6045

Hardware version 0100, 0200, 0201

Platform SYGDG1 Date 07-05-2018

Filename SYGDG1_2-0100to0201-6045-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex_Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface: {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Solved: When the second input is disturbed or disconnected the the first output was disturbed



0301, 0302, 0303, 0304 Hardware version

Platform SYGDG1B 05-03-2018 Date

Filename SYGDG1_2-0301to0304-6044-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

* Excessive audio restore time after video input interruption, when set to large F-delay value
* Improved tacking output pulse, when F-delay set to odd value (also for P50/P60 Sync two frame mode)

Software version 6044

0100, 0200, 0201 Hardware version

SYGDG1 **Platform** 05-03-2018 Date

Filename SYGDG1_2-0100to0201-6044-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

* Excessive audio restore time after video input interruption, when F-delay set to large value
* Improved tacking output pulse, when F-delay set to odd value (also for P50/P60 Sync two frame mode)

6042 Software version

Hardware version 0301, 0302, 0303, 0304

SYGDG1B Platform Date 28-08-2017

Filename SYGDG1_2-0301to0304-6042-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Fixes:

* Fixed two pixel shift for 720p60 format in synchronizer function.

* Added option to lock 1080p50 (1080p60) video output signal to 1080i50 (1080i60) tri-level sync.

* Fixed s352 field 2 errors for 1080i formats in case incoming video signal contains s352 packets in line 573 (by blanking s352 packets in line 573, before inserting s352 packets in line 10 and 572 in accordance with SMPTE ST 352-2013).



Hardware version 0100, 0200, 0201

Platform SYGDG1 28-08-2017 Date

Filename SYGDG1_2-0100to0201-6042-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

* Fixed two pixel shift for 720p60 format in synchronizer function.

* Added option to lock 1080p50 (1080p60) video output signal to 1080i50 (1080i60) tri-level sync.

* Fixed s352 field 2 errors for 1080i formats in case incoming video signal contains s352 packets in line 573 (by blanking s352 packets in line 573, before inserting s352 packets in line 10 and 572 in accordance with SMPTE ST 352-2013).

Software version 6040

0301, 0302, 0303, 0304 Hardware version

Platform SYGDG1B Date 18-01-2016

Filename SYGDG1_2-0301to0304-6040-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Fixed: Color-correction inlouding line 560 and line 1123.

Software version 6040

Hardware version 0100, 0200, 0201

Platform SYGDG1 Date 18-01-2016

SYGDG1_2-0100to0201-6040-service.zip Filename

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Fixed: Color-correction inlcuding line 560 and line 1123.

Software version 6039

Hardware version 0301, 0302, 0303, 0304

Platform SYGDG1B Date 07-09-2015

SYGDG1_2-0301to0304-6039-service.zip Filename

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex_Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface: {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Fixed: Ticking audio when using card with 60Hz video formats, introduced in version 6038.



Hardware version 0100, 0200, 0201

SYGDG1 **Platform** 07-09-2015 Date

Filename SYGDG1_2-0100to0201-6039-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Fixed: Ticking audio when using card with 60Hz video formats, introduced in version 6038.

6038 Software version

0301, 0302, 0303, 0304 Hardware version

SYGDG1B Platform 25-03-2015 Date

Filename SYGDG1_2-0301to0304-6038-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Fixed: Asynchronous Input switch handling. Fixed: Switching between SD and HD signals sometimes the audio stopped working

Software version 6038

Hardware version 0100, 0200, 0201

SYGDG1 Platform 25-03-2015

Filename SYGDG1_2-0100to0201-6038-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex_Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface: {.....Cortex_install_dir....}\Forms\Device\{filename.clf}

Fixed: Asynchronous Input switch handling. Fixed: Switching between SD and HD signals sometimes the audio stopped working

6034 Software version

0301, 0302, 0303 Hardware version SYGDG1B **Platform** Date 19-11-2013

SYGDG1_2-0301to0303-6034-service.zip **Filename**

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir....}\Forms\Device\(\frac{t}{1}\) Forms\Device\(\frac{t}{1}\) Final (CLF)

Fixed: CVBS input was not working on the last release. Fixed: Sporadic audio issues with some hardware, resulting in audio glitches.



0100, 0200, 0201 Hardware version

SYGDG1 **Platform** Date 19-11-2013

Filename SYGDG1_2-0100to0201-6034-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Fixed: CVBS input was not working on the last release. Fixed: Sporadic audio issues with some hardware, resulting in audio glitches.

Software version 5933

Hardware version 0100, 0200, 0201

Platform SYGDG1 16-07-2013 Date

SYGDG1_2-0100to0201-5933-service.zip **Filename**

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}Forms\Device\{filename.clf}

Fixed: Corruption of video, metadata and audio in passmode with hardware 0200. This was best seen as corrupted audio. Occurence rate was hardware dependent, some cards were unaffected, some were in moderation, others experienced more frequent (audio) corruption. In practice, this is due to the video memory corrupting data, including the passed (untouched) audio. This was introduced at version 5429, 4928 was OK.
Fixed: Dolby Guard band allignment. Now, when the dolby guard band is at the right place in time, the guard band is maintained at the correct position for asynchronous inputs.
Fixed: Increased minimum delay of framesync, delay is now equal to minimum delay of audio sync, when audio is set to processed.
Fixed: Increased minimum delay of framesync, delay is now equal to minimum delay of audio sync, when audio is set to processed.

Software version 5933

Hardware version 0301.0302 **Platform** SYGDG1B Date 16-07-2013

Filename SYGDG1_2-0301to0302-5933-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Included in this download are the Cortex Layout Forms (CLF files), please copy them to your cortex installation directory for the latest graphical user interface : {.....Cortex_install_dir.....}\Forms\Device\{filename.clf}

Fixed: Corruption of video, metadata and audio in passmode with hardware 0200. This was best seen as corrupted audio. Occurence rate was hardware dependent, some cards were unaffected, some were in moderation, others experienced more frequent (audio) corruption. In practice, this is due to the video memory corrupting data, including the passed (untouched) audio. This was introduced at version 5429, 4928 was OK. Fixed: Dolby Guard band allignment. Now, when the dolby guard band is at the right place in time, the guard band is maintained at the correct position for asynchronous inputs. Fixed: Increased minimum delay of framesync, delay is now equal to minimum delay of audio sync, when audio is set to processed. Fixed: Increased minimum delay of audio sync, when audio is set to processed.



0100, 0200 Hardware version **Platform** SYGDG1 25-09-2012 Date

SYGDG1_2-HFS-0100to0200-4928-service.zip Filename

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

- New Features:
 added audio handling feature to Blank, Pass or Process audio
 changed behaviour for switching outside the switchline position
 solved audio disturbance due to format switching

Software version 4827

0100, 0200 Hardware version **Platform** SYGDG1 05-09-2012 Date

Filename SYGDG1_2-HFS-0100to0200-4827-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

New Features:
- Solved missing LevelB option in 3G menu's

Software version 4326

Hardware version 0100, 0200 SYGDG1 Platform Date 01-05-2012

Filename SYGDG1_2-HFS-0100to0200-4326-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

New Features:

- Added active input status.
 Added Audio Phase alignment option
 Added Audio Phase alignment option
 Added automatic switch over functionality
 Changed tracking pulse to follow Frame Synchronizer delay only
- adjusted card output timing



0100, 0200 Hardware version **Platform** SYGDG1 Date 02-04-2012

Filename SYGDG1_2-0100to0200-4023-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

New Features:
- Added audio status bits overwrite to ensure correct audio status bits.

Software version 3922

Hardware version 0100, 0200 SYGDG1 **Platform** Date 17-02-2012

Filename SYGDG1_2-0100to0200-3922-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

New Features:
- Solved audio disturbance on format change

Software version 3820

Hardware version 0100.0200 SYGDG1 **Platform** Date 08-12-2011

Filename SYGDG1_2-HFS-0100to0200-3820-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

New Features: - Added Level-B 3G output mapping

Software version 3217

Hardware version 0100, 0200 **Platform** SYGDG1 Date 13-10-2011

SYGDG1_2-HFS-0100to0200-3217-service.zip **Filename**

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

New Features:

- Copy time code from single input to both outputs
 Switch output on input loss
 Added 1 and 5 seconds freeze option



Hardware version 0100, 0200 Platform SYGDG1 31-01-2011 Date

SYGDG1_2-HFS-0100to0200-1913-service.zip Filename

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Fixed bugs: Card into boot mode when zoneplate was selected on AUTO-A input format.

Software version 1713

0100, 0200 Hardware version Platform SYGDG1 Date 17-11-2010

Filename SYGDG1_2-HFS-0100to0200-1713-service.zip

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Fixed bugs: ANC Data Processing not transparent Improved frame/field freeze on imput loss.

Added features: Action on input loss. Timecode per output channel.

Software version 1512

Hardware version 0100, 0200 Platform SYGDG1 29-09-2010

Filename SYGDG1_2-HFS-0100to0200-1512-service.spf

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Fixed bugs: No audio in SD-SDI.



Hardware version 0100, 0200 Platform SYGDG1 Date 04-08-2010

SYGDG1_2-0100to0200-1511-service.zip Filename

This card may be updated through the front ethernet connector on the card, which is quicker as compared to programming with Cortex / Synapse setup. For instructions please refer to the Synapse module reprogramming guide (available on http://www.axon.tv/EN/support/downloads/manuals) under Chapter 2 re-programming Linux based cards or contact support.

Fixed bugs:

CRC error on output with format change or bad signals on the input.

The VITC timecode frame counter was lagging the seconds counter.

RGB gain control order corrected.

Audio corruption after no or bad input signal.

When switching the input signal, or connecting signal via a patch pannel, or booting up equipment feeding the card, the audio and/or video might stop working. Also, the channel order of the audio might get corrupted.

A couple of audio errors (mute) per hour in SD-SDI when Audio embedding is off. (audio through frame sync)

Extra write pointer set added at start of active picture to handle switching errors more than one line. Only for 1080i59.94, 1080i50, 720p60 and 720p50.

ANC blanked on input loss.

EDH added for SD-SDI.

Card in goes into minimum delay (3 lines) when set to input lock.

Audio delay above 85ms works now correct

Added features: OSD text added. Manual freeze added Auto format added

0902 Software version

Hardware version 0100, 0200 Platform SYGDG1 Date 11-12-2009

Filename SYGDG1_2-0100to0200-0902-service.zip

First full release.

Fixed: Correct analog value of switch line detection Fixed: Audio of input 2 embedded on output 1, and audio of input 1 on output 2